

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

James Herbert Kukula et al.

Appl. No. 10/046,220

Filed: January 16, 2002

For: **METHOD AND APPARATUS FOR  
FORMALLY CONSTRAINING  
RANDOM SIMULATION**

Conf. No. 4032

Art Unit: 2185

Examiner: TBD

Atty. Docket: 06816.0110

Cust. No. 35795

**Information Disclosure Statement**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

The Examiner is requested to consider the documents listed on accompanying Form PTO/SB/08B and make this information of official record in the application.

Applicants have listed publication dates on the attached PTO/SB/08B based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

Applicants reserve the right, with respect to any of the information provided herewith, to prove that this information may not be prior art and/or to prove that this information may not be enabling for the teachings purportedly offered.

This statement should not be construed as a representation that a search has been made, or that information more material to the examination of the present patent application does not exist. The Examiner is specifically requested not to rely solely on the material submitted herewith.

Applicants have checked the appropriate boxes below.

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Applicants Kukula *et al.*  
Appl. No. 10/046,220

- ☒ 1. This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No statement under 37 C.F.R. § 1.97(e) or fee is required, or
- ☐ 2. This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection or Notice of Allowance, or action that otherwise closes prosecution in the application, and
- ☐ a. I hereby state that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1), or
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- ☐ c. Attached is our Check No. \_\_\_\_\_ to cover the payment of the \$180.00 fee under 37 C.F.R. § 1.17(p).

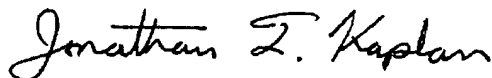
It is respectfully requested that the Examiner initial and return a copy of the enclosed PTO/SB/08B, and to indicate in the official file wrapper of this patent application that the documents have been considered.

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The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 502584 referencing docket number 06816.0110.

Respectfully submitted,



Jonathan T. Kaplan (Reg. No. 38,935)

Date: Mar. 6, 2004  
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PTO/SB/088 (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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Substitute for form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>		<b>Complete if Known</b>	
		Application Number	10/046,220
		Filing Date	January 16, 2002
		First Named Inventor	James Herbert Kukula
		Art Unit	2185
		Examiner Name	
Sheet	1	of	1
		Attorney Docket Number	06816.0110

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	1	H. Cho, G. D. Hachtel, E. Macii, B. Plessier, and F. Somenzi. "Algorithms for Approximate FSM Traversal." Proc. 30th ACM/IEEE Design Automation Conference, pp. 25-30, 1993.	
	2	H. Cho, G. D. Hachtel, E. Macii, M. Poncino, and F. Somenzi. "A Structural Approach to State Space Decomposition for Approximate Reachability Analysis." In Proc. IEEE Intl. Conf. on Computer Design (ICCD), October 10-12, 1994, pp236-239.	
	3	H. Cho, G. D. Hachtel, E. Macii, M. Poncino, and F. Somenzi. "Automatic State Space Decomposition for Approximate FSM Traversal Based on Circuit Analysis." IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 15, No. 12, December 1996, pp1451-1464.	
	4	H. Cho, G. D. Hachtel, E. Macii, B. Plessier, and F. Somenzi. "Algorithms for Approximate FSM Traversal Based on State Space Decomposition." IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 15, No. 12, December 1996, pp1465-1478.	
	5	C. H. Yang and D. L. Dill. "Validation with Guided Search of the State Space." In Proc. of the Design Automation Conf., June 1998, pp599-604.	
	6	Govindaraju, G. S. and Dill, D. L. "Verification by Approximate Forward and Backward Reachability." IEEE/ACM ICCAD, pp. 366-370, Nov. 8-12, 1998.	

Examiner Signature	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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